

FIG 1

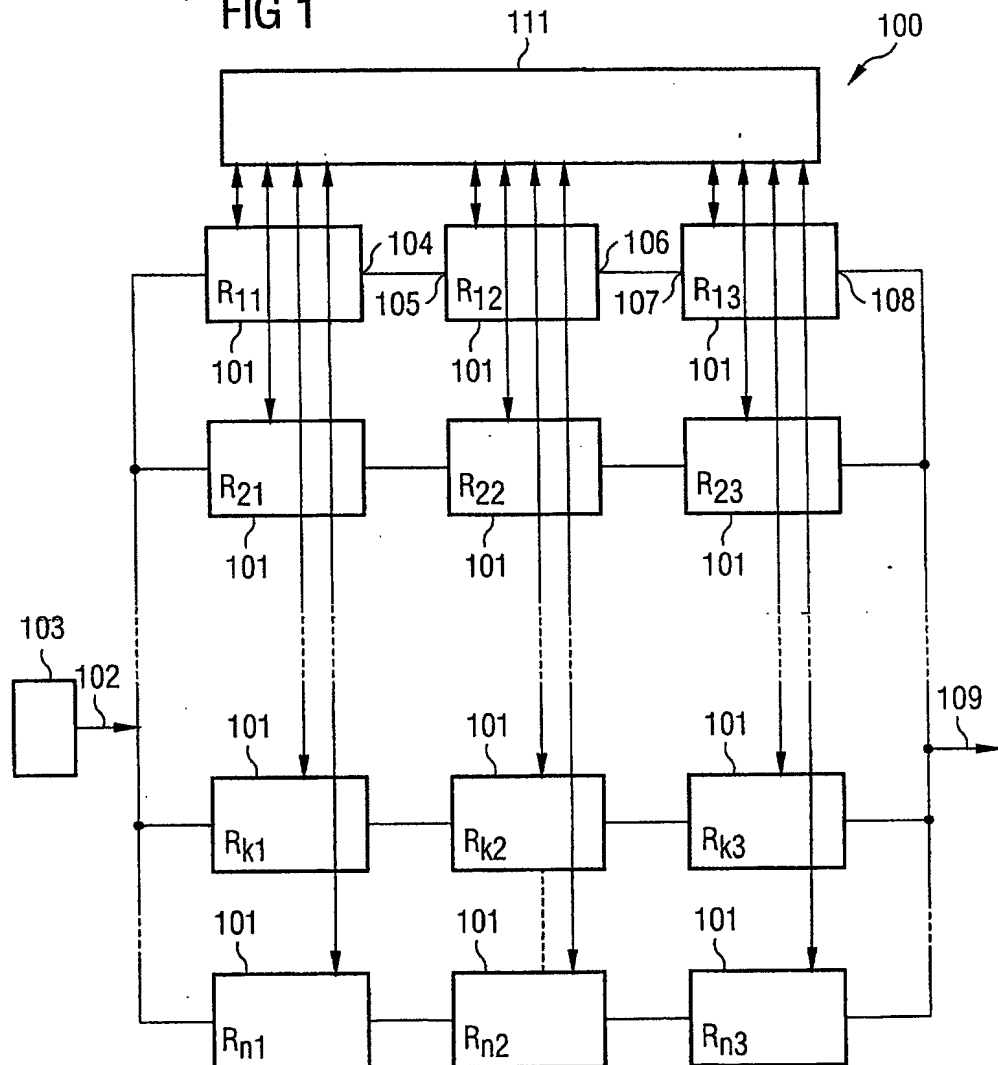


FIG 2

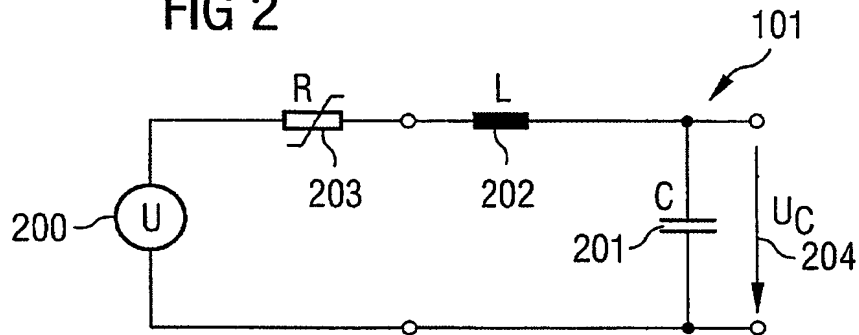


FIG 3

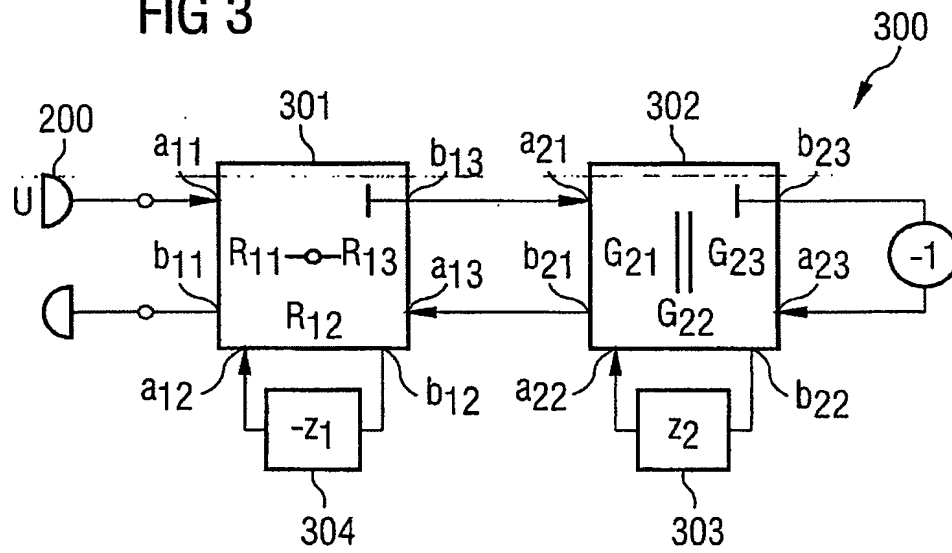


FIG 4

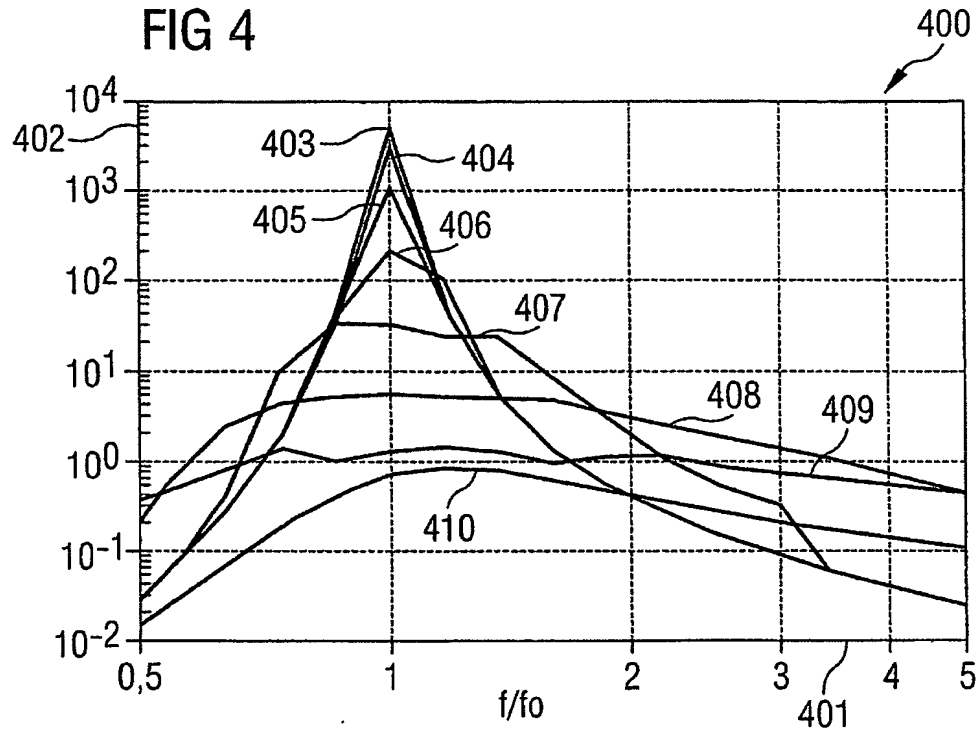


FIG5

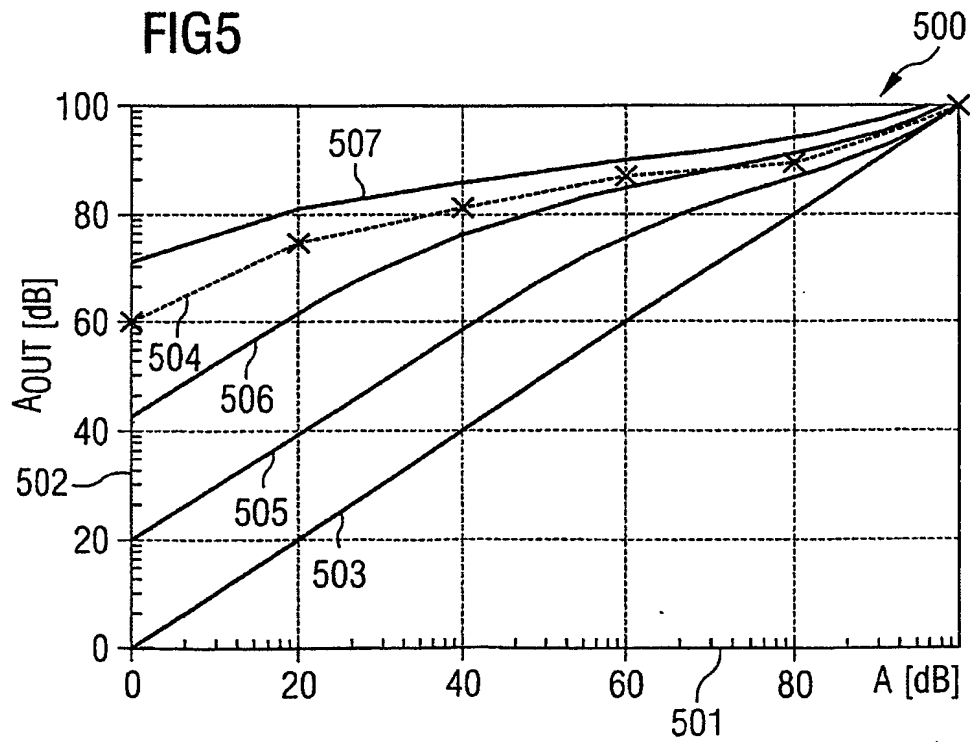
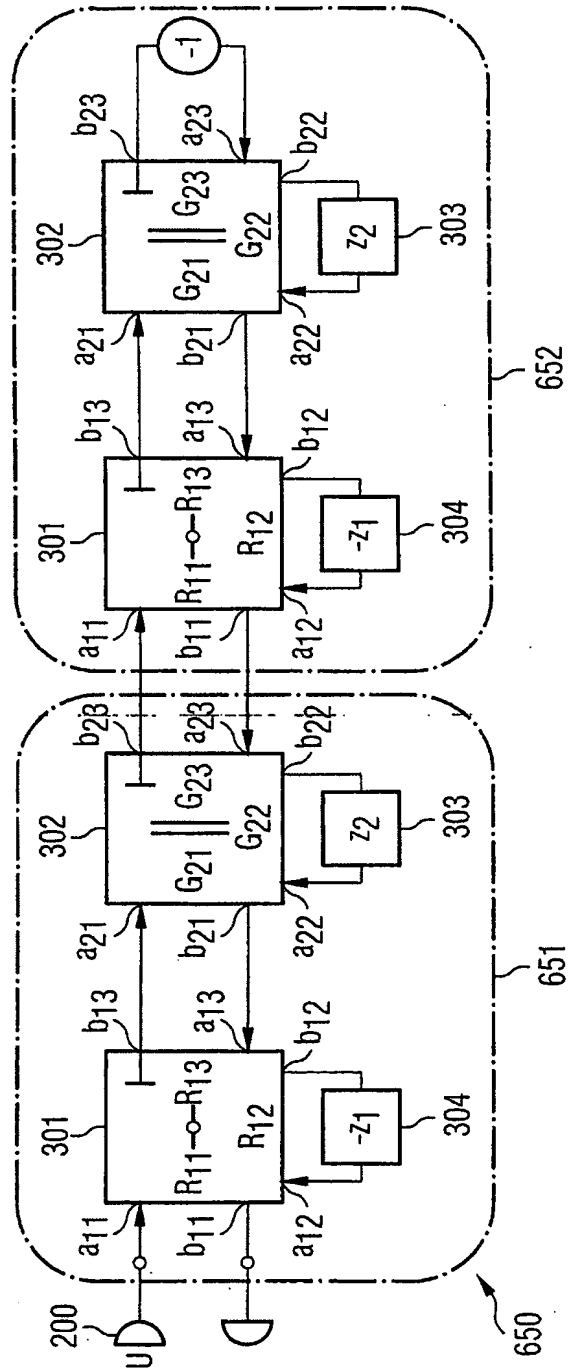


FIG 6A

The diagram shows a two-port network 600. The input port 601 is connected to a voltage source U (200). The network consists of a series resistor R1 (203) followed by a shunt inductor L1 (202). This is followed by a series resistor R2 (203) and a shunt inductor L2 (202). The output port 602 is connected to a load capacitor C2 (201). The input voltage is U_{C1} (204) and the output voltage is U_{C2} (204).



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